2018 Roadmap Graphics – Side 1

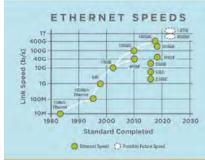
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February 27 | 2018



2018 ETHERNET ROADMAP

THE PAST, PRESENT AND FUTURE OF ETHERNET





INTEROPERABILITY AND CERTIFICATION

The Ethernet Alliance is committed to leading the charge to instilling industry confidence in Ethernet standards through its multivendor interoperability demonstrations and plugfests, Our PoE Certification-Program takes this mission to the next level!

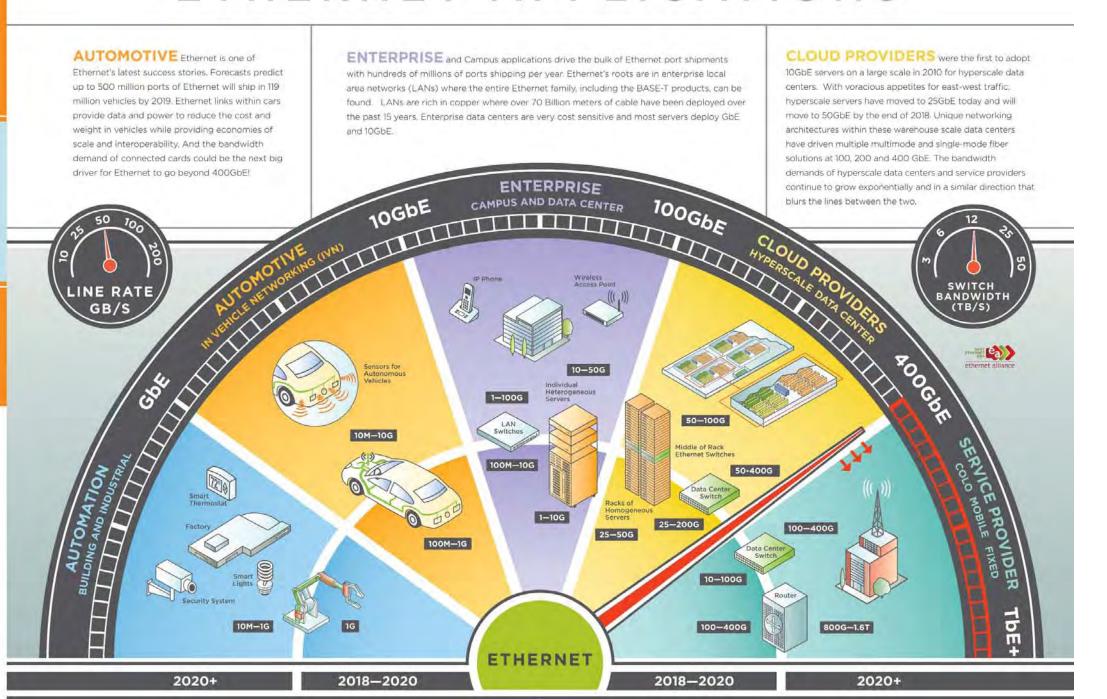
Our industry-defined PoE Certification Test Plan is based on the Ethernet PoE standard, and products passing this test will be granted the Ethernet Alliance PoE Certification Logo. This logo will provide instant recognition for products that based on the IEEE 802.3 PoE standard, and provide confidence in the multi-vendor interoperability of those products bearing it. The logos will also provide clear guidance on which devices will work with each other.

The first generation of the program certifies Type I and Type 2 products that use 2-Pair of wires. The second generation of the program will tackle the forthcoming IEEE 802.3bt PoE standard. This table explains the capabilities of the Types.

Роб Туров ана Фалана			POE4 E - TYP	a-Pair PoE in Standardiestion						
Client	0	4	2	1	A	5	8	1	1	
PSE Power (W)	15.4	340	7	15.4	20	45	60	75	90	
PD Power (W)	11	3,84	6.48	111	16.6	40	61	62	71.0	
		4-Pule Pull-Type 3						4 Pair Pat Type 4		



ETHERNET APPLICATIONS



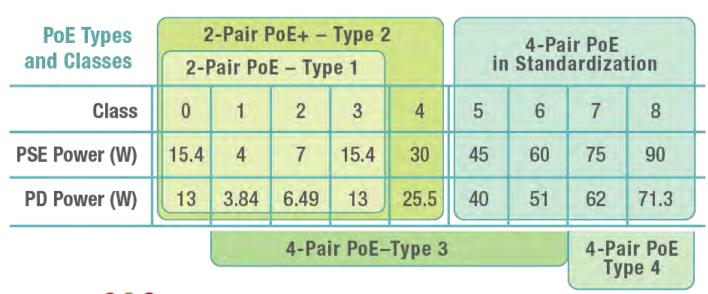
BUILDING AND INDUSTRIAL applications highlight the need for lower speed

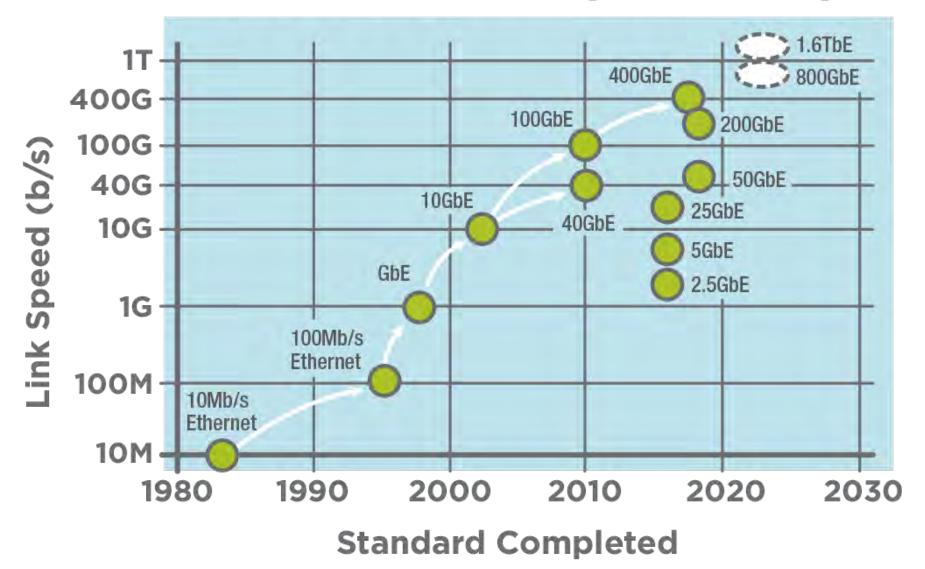
Ethernet solutions in harsh environments. The Ethernet community is working to define a single standard for 10 Mb/s operation plus power delivery over a single twisted pair. This will consolidate a landscape of multiple legacy protocols, driving the promise of Ethernet's multi-level interoperability to new heights for these spaces, as 2019 forecasts point to 165 million ports per year.

SERVICE PROVIDERS have driven higher speed Ethernet solutions for decades. Router

connections, client side optics for optical transport networks (OTN) equipment, and wireless backhaul have continually pushed Ethernet to higher rates and distances to meet the demands for wireless connectivity. And with global demand by consumers for video, this shows no signs of changing

ETHERNET SPEEDS

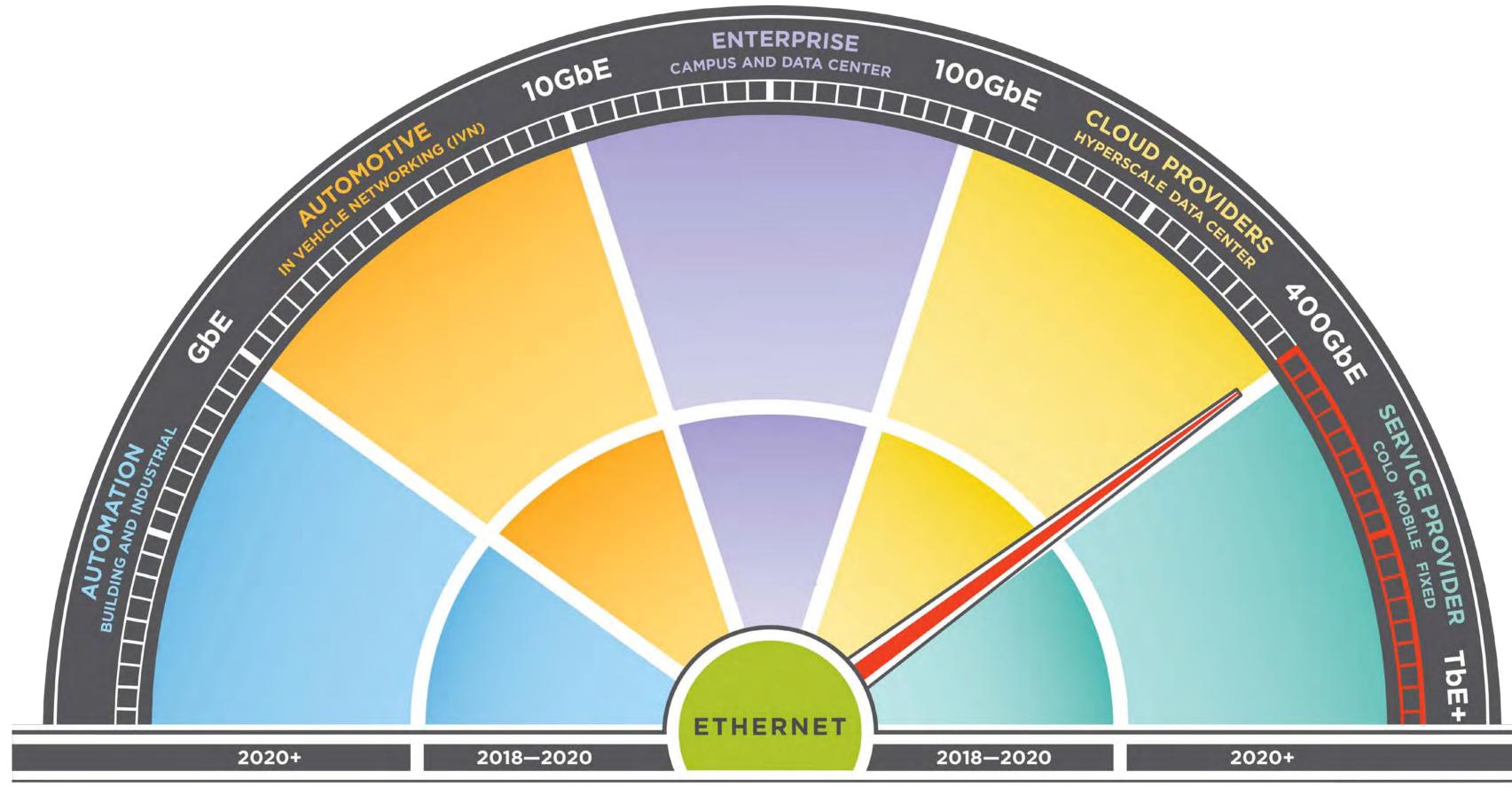




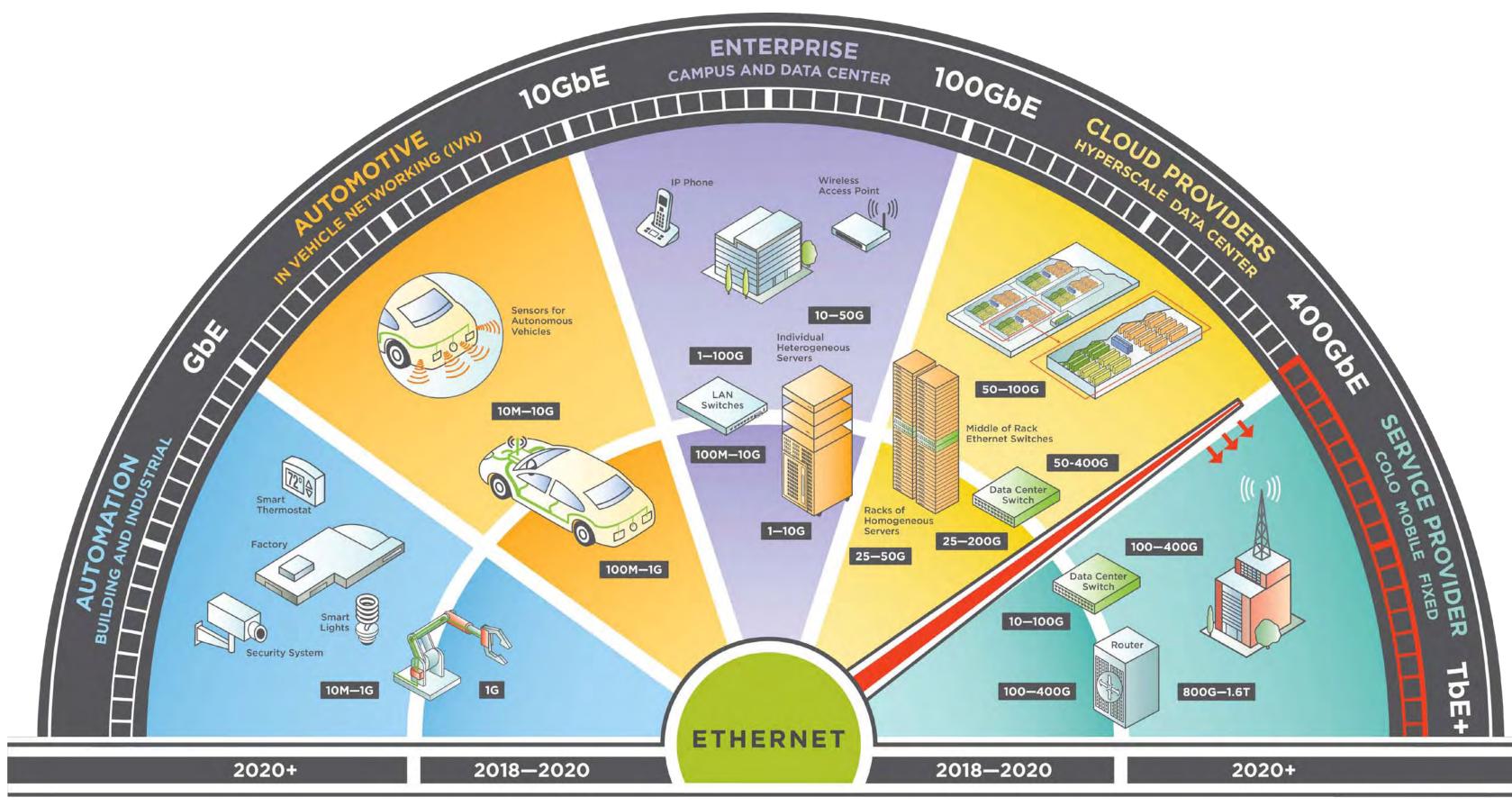


Ethernet Speed Possible Future Speed

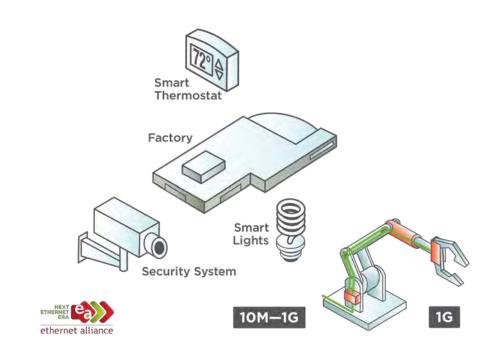


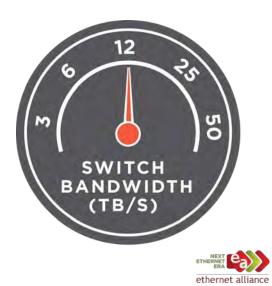




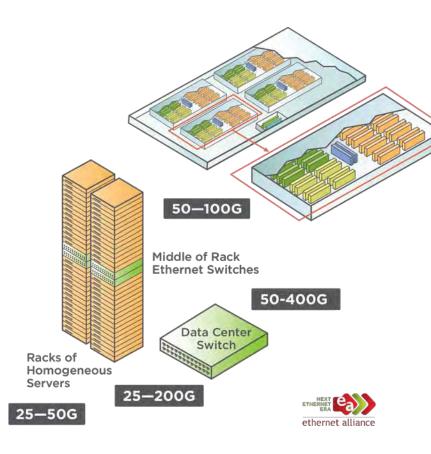


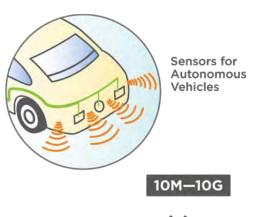








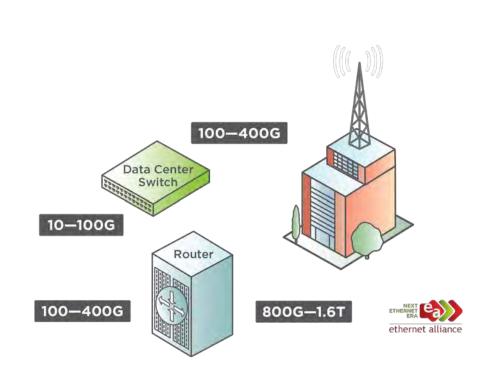


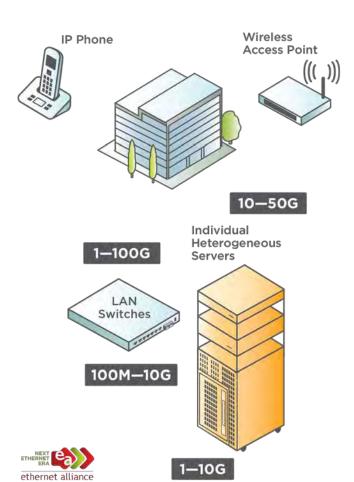


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100M-1G





Exit Text

Thank You



2018 Roadmap Graphics – Side 2

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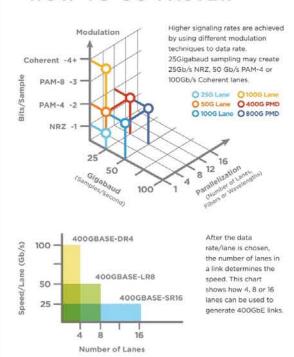


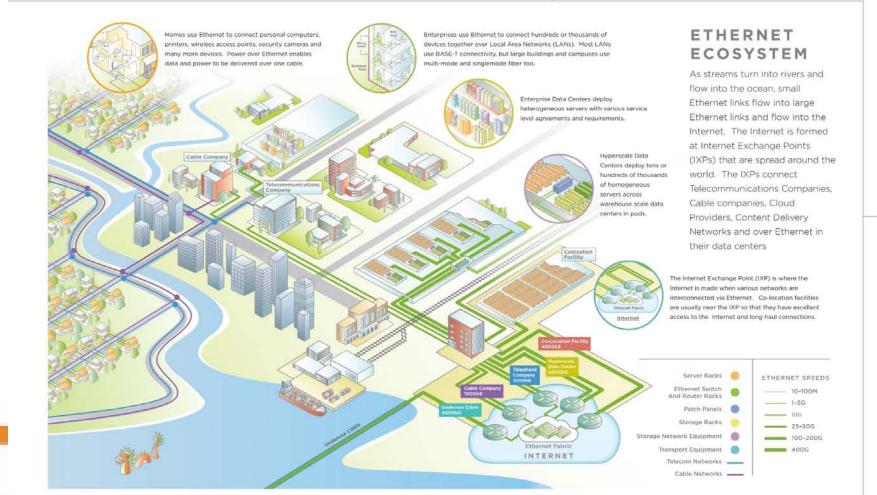
EMERGING INTERFACES AND NOMENCLATURE

	Electrical Interface		Twonax Cable	Twisted Pair (1 Pair)	Twisted Pair (4 Pair)		500m P5M4	2km SMF	10km SMF	40km SMF	10km SMF
10BASE-		TIS?		TIS/TIL							
TOOBASE-				TI							
1000BASE-				7.71	T						
2.SGBASE-		кх		7357	T						
SGBASE-		KR		TIS?	т						
10GBASE-				TIS?	T						
25GBASE-	25GAUI	KR	CR/CR-S		T	SR			LR	ER	
40GBASE-	XCAUI	(KR4)	CR4		(T.)	SR4/eSR4	PSM4	FR	LR4	ER4	
50GBASE-	LAUI-2/50GAUI-2 50GAUI-1	KR	CR			SR		FR	LR	ER	
100GBASE-	CAUI/10 CAUI-4/100GAUI-4 100GAUI-2 100GAUI-1	KR4 KR2 KRI	CR10 CR4 CR2 CR1			SRIO SR4 SR2	PSM4	10X10 CWDM4 CLR4 100G-FR	LR4 4WDM-10 100G-LR	ER4 4WDM-40	12:
200GBASE-	200GAUI-4 200GAUI-2	KR4 KR2	CR4 CR2			SR4	DR4	FR4	LR4	7	7
400GBASE-	400GAUI-16 400GAUI-8 400GAUI-4	KR4	CR4			SR16	DR4	FR8 400G-FR4	LR8	7	9

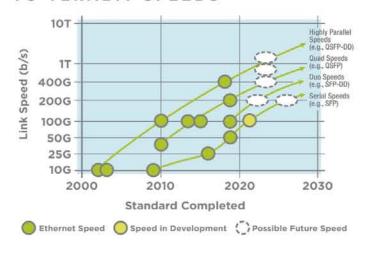
Gray Text = IEEE Standard Red Text = In Standardization Green Text = In Study Group
Blue Text = Non-IEEE standard but complies to IEEE electrical interfaces

HOW TO GO FASTER



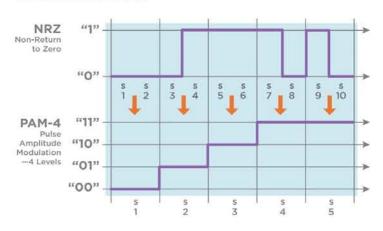


TO TERABIT SPEEDS



SIGNALING METHODS

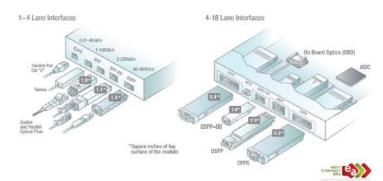
Most high speed Ethernet signaling has been Non Return to Zero (NRZ), but Pulse Amplitude Modulation 4 Level (PAM-4) signaling delivers twice as many bits per sample.



FORM FACTORS

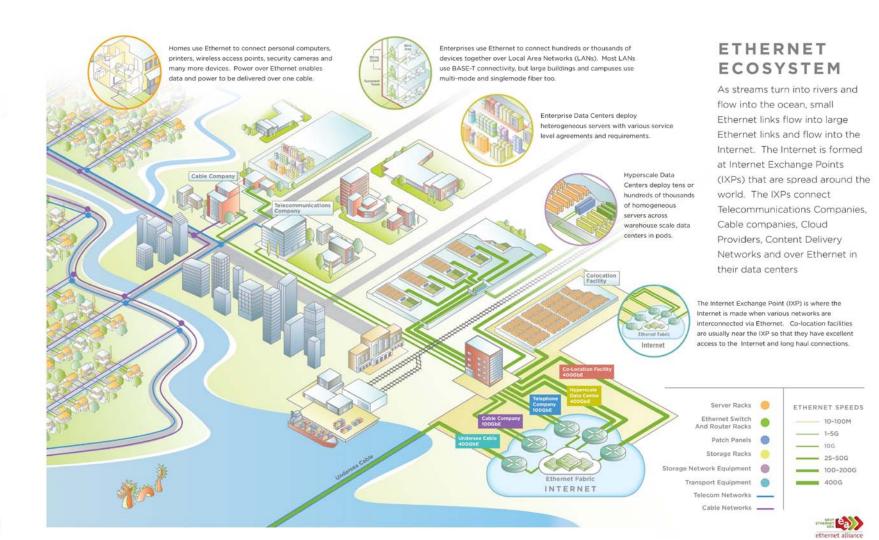
This diagram shows the most common form factors used in Ethernet ports. Hundreds of millions of RJ45 ports are sold a year while tens of millions of SFP and millions of QSFP ports ship a year.

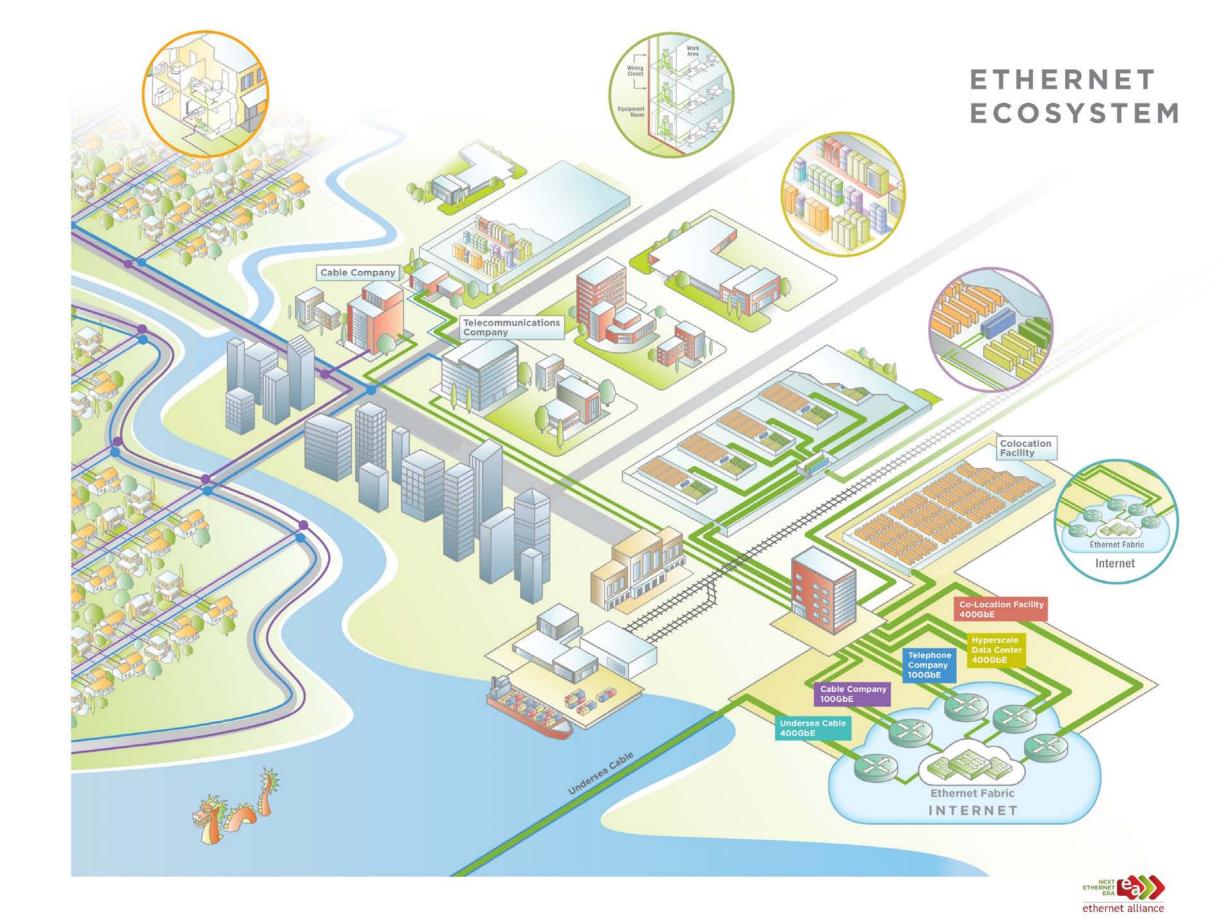
This diagram shows new form factors initially designed for 100GbE and 400GbE Ethernet ports. All have 4 or 8 lanes and the OBO has up to 16 lanes. The power consumption of the modules is proportional to the surface area of the module.







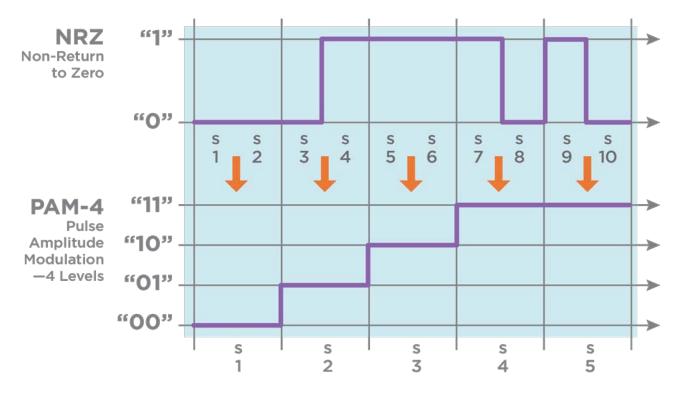






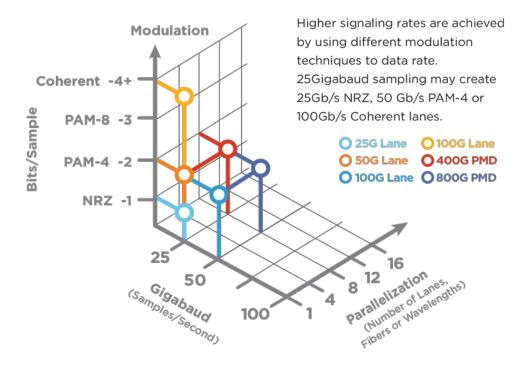
SIGNALING METHODS

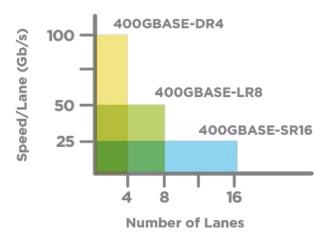
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HOW TO GO FASTER





After the data rate/lane is chosen, the number of lanes in a link determines the speed. This chart shows how 4, 8 or 16 lanes can be used to generate 400GbE links.



EMERGING INTERFACES AND NOMENCLATURE

	Electrical Interface	Backplane	Twinax Cable	Twisted Pair (1 Pair)	Twisted Pair (4 Pair)	MMF	500m PSM4	2km SMF	10km SMF	40km SMF	80km SMF
10BASE-		T1S?		T1S/T1L							
100BASE-				T1							
1000BASE-				T1	Т						
2.5GBASE-		кх		T1S?	Т						
5GBASE-		KR		T1S?	Т						
10GBASE-				T1S?	Т						
25GBASE-	25GAUI	KR	CR/CR-S		Т	SR			LR	ER	
40GBASE-	XLAUI	KR4	CR4		Т	SR4/eSR4	PSM4	FR	LR4	ER4	
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200GBASE-	200GAUI-4 200GAUI-2	KR4 KR2	CR4 CR2			SR4	DR4	FR4	LR4	?	?
400GBASE-	400GAUI-16 400GAUI-8 400GAUI-4	KR4	CR4			SR16	DR4	FR8 400G-FR4	LR8 ?	?	?

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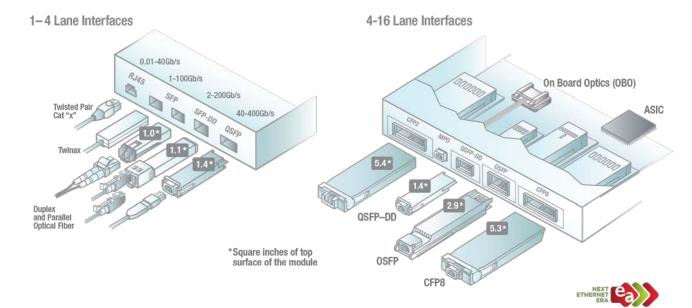
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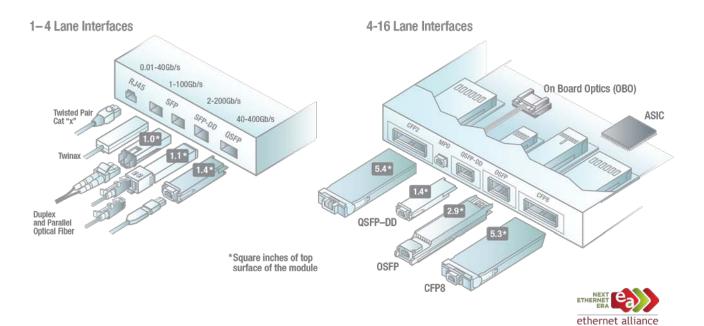
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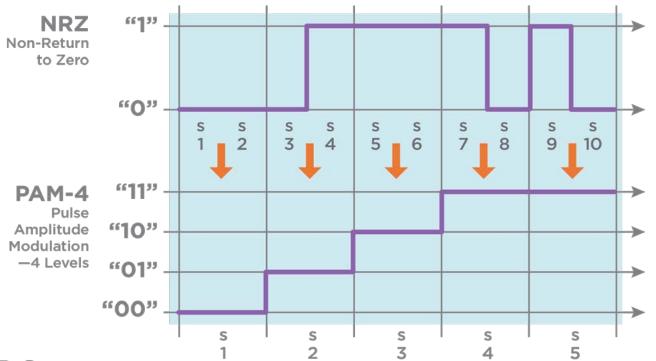
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FORM FACTORS



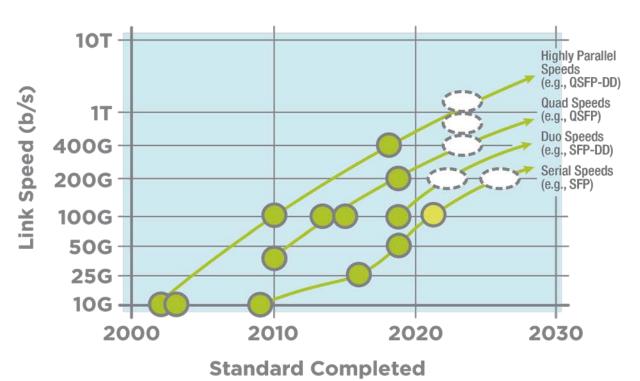
SIGNALING METHODS



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TO TERABIT SPEEDS

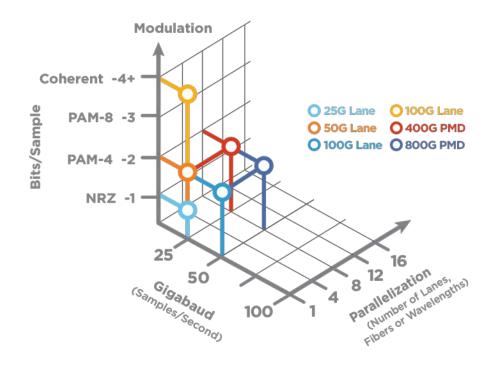
Ethernet Speed

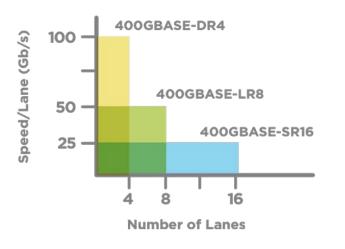


Speed in Development Possible Future Speed



HOW TO GO FASTER





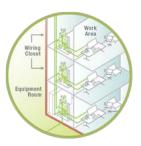
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Exit Text

Thank You

